

Figure 1

receive a system management interrupt acknowledge signal from a processor 210

fetch a plurality of system management interrupt handler instructions from an integrated system management memory in a memory controller in response to the system management interrupt acknowledge signal 220

Figure 2

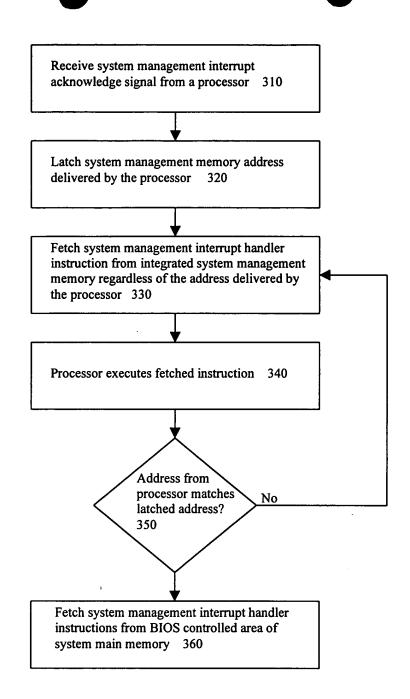


Figure 3